



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: KURATOMI et al

Serial No.: 10/682,028

Filed: October 10, 2003

For: Fabrication Method Of Semiconductor
Integrated Circuit Device

Art Unit:

Examiner:

SUPPLEMENTAL PRELIMINARY AMENDMENT

Mail Stop: Amendment (No Fee)
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 29, 2003

Sir:

The following preliminary amendments and remarks are respectfully
submitted in connection with the above-identified application.

Amendments to the Specification and Abstract

Remarks are included following the amendments